

Table of PADS Maker Schematic Error Messages

The following table lists all possible error messages in **PADS Maker Schematic**, describes the cause of each message, and prescribes a solution for fixing the error.

Error Number	Message	Cause	Solution
5617	Not parsable — missing token.	PCB Configuration file has an invalid token, such as a misspelled keyword, invalid delimiter, or erroneous syntax.	Check for errors with keywords, delimiters, whitespace. See also 5621.
5619	Unable to open file [filename].	File does not exist in the current working directory or WDIR path.	Copy the file [filename] to the working directory or to the location specified by your WDIR variable. Specify your WDIR variable to the location of [filename].
5621	Unrecognized token [token].	PCB Configuration file has unrecognized token, such as an unsupported keyword.	Check for misspelled keywords. If you have modified the original configuration file that was installed for this PCB interface, refer to a configuration file for one of the other PCB interfaces to verify keyword spelling.
5625	Unable to open file [filename].	File does not have read permission.	Check permissions, correct as needed.
5629	[value] not in yes-no table.	Token value is other than yes or no.	Use only "yes" or "no."
5669	[string] not status word.	Exclude keyword has value which is something other than: Error, Note, Warning.	Correct value for Exclude keyword.
5670	Error exclusions in effect.	Some warnings, errors or notes are being excluded from being displayed.	Check ExcludeNums keyword in the PCB configuration file.
5693	Wrong number of tokens.	A keyword in the PCB configuration file is missing some tokens.	Check for misspelled keywords.
5701	Invalid input for [function_name] operation.	Input to one of the internal functions is incorrect. This may be caused by symbols missing some of the required PCB attributes.	Check for required attributes on symbols, correct as needed.
5705	Error adding object %s of type %s to object %s of type %s.	Mismatch of object types.	Check objects for incorrect or missing attributes.
5708	Could not open file %s for %s access.	The specified file could not be opened for the specified access.	Ensure that the specified file has proper write privileges.
5709	Netlister cannot access the WIR file.	You do not have read/write permissions to the WIR file.	Check and fix the permissions to the WIR file.

5711	Could not load ViewBase for %s %s.	Bad or missing wire file, which can be caused by one of the following: <ul style="list-style-type: none"> • A composite Symbol without an underlying Schematic. • An error in the wire file. 	Try one of the following: <ul style="list-style-type: none"> • If there is no underlying schematic, change the Symbol Block type to Module in PADS Maker Schematic. • Open the wire file in a text editor and search for the string called "error." Correct the problem you identified in the schematic (not directly in the wire file).
5714	Object type %s not valid for %s operation on object type %s.	Mismatch of object types.	Check the object for incorrect or missing attributes.
5716	Could not set %s for object %d of type %s.	Mismatch of object types.	Check the object for incorrect or missing attributes.
5727	Global net %s on subcircuit %s is being ignored.	A global net is implicitly aliased as a named net.	Use a bus ripper to alias a global net to another named net.
5728	Using signal name from level above.	A use of global nets that conflicts with local net names used elsewhere in the design.	Verify that all occurrences of a specific net name are either local or global, but not mixed.
5736	Could not find object %s of type %s on schematic %s.	An object is no longer on the schematic. This may be because of edits made to the schematic after the PCB netlist file was exported to the place & route tool.	Correct the schematic or the PCB database, or both, and try back-annotation again.
5758	Error while loading object of type %s.	A problem with an object.	Check the object for missing or incorrect attributes.
5773	Manually disconnect net.	You deleted a net in PCB layout, and it does not support this change automatically when you back-annotate to a schematic.	Correct this problem manually in the schematic.
5774	Manually connect to old net %s.	You made a net connection in PCB layout, and it does not support this change automatically when you back-annotate to a schematic.	Correct this problem manually in the schematic.
5775	Manually connect to new net %s.	You made a net connection in PCB layout, and it does not support this change automatically when you back-annotate to a schematic.	Correct this problem manually in the schematic.
5776	Manually delete component.	You made a component deletion in PCB layout, and it does not support this change automatically when you back-annotate to a schematic.	Correct this problem manually in the schematic.
5784	Back-annotation out of sync.	The back-annotation from/to list does not match the object identifiers on the current version of the schematic.	Back-annotate to the version used to generate the current PCB netlist file.
5791	Bad %s dictionary file at line %d.	A discrepancy between the dictionary files and the current version of the schematics.	Back-annotate to the version used to generate the current PCB netlist file.
5792	%s dictionary entry %s at line %d was deleted.	You deleted an object from the schematics and the software uses this warning to inform you that it updated the dictionary file.	No action required.

5793	Attribute %s found in both Aliasing and Translation configuration.	Attribute Aliasing and Translation are mutually exclusive, but the software found an attribute in both of these sections of the PCB configuration file.	Correct the PCB configuration file. The attribute should be in only one of these sections.
5795	No shape specified.	The PCB library does not have a shape specified for the components PKG_TYPE attribute.	Correct either the PCB library or the PKG_TYPE attribute value.
5800	Gpi Initialization Failed. Aborting run.	Could not initialize and load schematic database.	Delete the wire files and run Check Project from within PADS Maker Schematic to regenerate new wire files.
5801	vbLsulnit() already Initialized. Returning old handle.	Re-initializing schematic database.	No action required.
5802	Error allocating memory for object of type object_type.	You are low on memory.	Close some applications and rerun the interface.
5806	Component %s not found on sheet 1 of design prototype.	PADS Maker Schematic does not support adding capacitors within the layout using ECO mode.	Not available (N/A).
5810	No schematic sheet in memory.	The current sheet was not found in memory.	Check to make sure a schematic sheet exists. If you deleted the schematic, delete the corresponding wire file.
5816	Unable to initialize LSU for null design name.	The design name passed for back annotation is a null string.	Correct the back-annotation filename so that it agrees with the schematic name.
5817	Unable to open filename.baf for writing.	Write permission lacking.	Check to see if you have write permission in the current working directory.
5818	Could not load schematic schematic_name, missing or bad data.	The schematic file does not exist for this project, or read/write permissions are incorrect.	Regenerate the schematic files by checking your project from within PADS Maker Schematic, and make sure you have read/write permissions.
5819	Use message ID (5819) to help identify message.	The baf file specified in the command line does not exist in the current working directory.	Copy the specified baf file to the current working directory.
5822	Could not find component/net instance (id).	You are trying to back annotate attributes to a net or component that is not in the schematic.	Check to see if the component or net instance (id) is in the specified schematic. Make sure that the schematic is written to the disk when any change is made to it. If you still have this problem then it is possible that the logical database and physical database are out of sync.
5823	Pin (pin_name) of instance (instance_name) on schematic (schematic_name) was not found.	A pin is missing its label.	Check to see if all pins on a symbol are labeled properly.
5826	Segment number (#) of net (net_name) on schematic (schematic_name) was not found.	You are trying to back annotate attributes to a segment which does not exist.	Check to see if logical and physical databases are in sync.

5834	Instance id on Schematic schematic_name does not exist. Check uid string uid - sheet_name.	You tried to back-annotate to a component that is not on the schematic.	Check to see if the component (id) is in the specified schematic. Make sure you write the schematic to the disk when you make any changes. Make sure you back-annotate to the version of the schematic you used to make the current PCB layout netlist file.
5840	Use message ID (5840) to help identify message.	The schematic being updated no longer exists or do not have write permissions to this schematic.	Check to make sure the schematic exists, and that you have write privileges to the file.
5841	Use message ID (5841) to help identify message.	The attribute you are updating no longer exists on this instance, possibly due to a change in the schematic from running PCB interface tools.	N/A.
5842	Use message ID (5842) to help identify message.	You do not have write permission to sheet #.	Change the permissions on sheet # so you can back annotate.
5848	Use message ID (5848) to help identify message.	You are trying to create a new sheet # when sheet # already exists.	Re-create the .baf file before attempting back annotation again.
5854	Instance value (%s) was added to instance (%s) on (%s)	This is information that design was modified during packaging. In this specific case it means that instance level property value was added to the component instance.	N/A.
5855	OATs settings in the viewdraw.ini and PCB configuration files are inconsistent.	The viewdraw.ini and PCB configuration files have different settings for OATs.	If you are using the user interface, then a dialog box appears in which you can change the OATs settings in the viewdraw.ini file. Otherwise, a failure will occur and you will need to change the OATs settings in either the viewdraw.ini file or the PCB configuration file.
5856	Unable to save schematic %s. Check if it is locked.	This schematic is currently locked by another process and is being modified. The lock file in the lock directory under the sch sub-dir contains the necessary information.	Ensure there is nothing highlighted in the schematic.
5958	Bad Detach for F2038.	Can be a difference in the pin labels between PADS Maker Schematic and the layout system.	Correct any differences between the pin labels on the schematic symbols and the pins on the geometries.
6000	Could not open file [filename].	The indicated file is not readable or writable.	Depending on the severity of the message, either ignore the message or verify that the directory exists, the file exists and/or the correct access modes are set.

6001	vpbGet [variables] failed.	A line in the PCB configuration file (PKG class="LAB") has been commented out, i.e. has a vertical bar () at the beginning of the line.	Remove the comment character to enable the line.
6002	SEC: Required license not found.	A valid license for the indicated product was not found.	Verify that the license file or license server has valid licenses for the products you are using.
6004	Could not write file [filename].	Unable to open the .baf file for writing.	Check the privileges on the current directory to ensure you have write privileges.
6005	Illegal regular expression: Code: [n].	A regular expression check at the indicated line in the PCB configuration file has improper syntax.	Correct the syntax. PCB Configuration File: CHKVAL_REG_EXP
6006	Component signal pin # not on symbol.	A SIGNAL or NC attribute was added to a component, but no corresponding SIGNAL or NC attribute appears on the symbol. The pin will not be added.	Add the attribute to the symbol or enable the automatic fix-up. PCB Configuration File: CHKBRD_BAD_SIG_NUM.
		You added or removed an NC attribute after you packaged the design. The PCB interface cannot change the component level attribute once the design has been packaged.	Remove the NC attribute and run pcbfwd again to repackage the design.
6007	Component signal pin added.	A SIGNAL or NC attribute was added to a component, but no corresponding SIGNAL or NC attribute appears on the symbol. The pin will be added anyway.	Add the attribute to the symbol or ignore or disable the message. PCB Configuration File: CHKBRD_BAD_SIG_NUM.
6009	vbChkAttGen: Multiple generators for attribute [name].	The GENVAL keyword appears more than once in the PCB configuration file for the same attribute.	Remove one of the offending GENVAL lines. PCB Configuration File: GENVAL.
6013	Unknown procedure: [procedure].	A CHKVAL, GENVAL, or CHKBRD keyword is followed by an unknown procedure name.	Refer to the standard configuration file and use only the documented procedure names. PCB Configuration File: CHKVAL, GENVAL, CHKBRD.
6016	Too many arguments on line.	There are too many arguments on the PCB configuration file line indicated. Some arguments will be ignored.	Reduce the number of arguments on the line.
6017	Ambiguous keyword: [keyword].	The indicated keyword in the BeginChkRules section of the PCB configuration file is ambiguous.	Refer to the standard PCB configuration file and use only the documented keywords.
6018	Ambiguous object type: [type].	The indicated object type in the BeginChkRules section of the PCB configuration file is ambiguous.	Refer to the standard PCB configuration file and use only the documented object type keywords.
6019	Ambiguous attribute name: [name].	The indicated attribute in the BeginChkRules section of the PCB configuration file is ambiguous.	Refer to the standard PCB configuration file and use only the documented attributes.

6020	Ambiguous severity: [severity].	The indicated error severity in the BeginChkRules section of the PCB configuration file is ambiguous.	Refer to the standard PCB configuration file and use only the documented error severity keywords.
6021	Unrecognized keyword: [keyword].	The indicated keyword in the BeginChkRules section of the PCB configuration file is unrecognized.	Refer to the standard PCB configuration file and use only the documented keywords.
6022	Unrecognized object type: [type].	The indicated object type in the BeginChkRules section of the PCB configuration file is unrecognized.	Refer to the standard PCB configuration file and use only the documented object type keywords.
6023	Unrecognized attribute name: [name].	The indicated attribute in the BeginChkRules section of the PCB configuration file is ambiguous.	Refer to the standard PCB configuration file and use only the documented attributes.
6024	Unrecognized severity: [severity].	The indicated error severity in the BeginChkRules section of the PCB configuration file is unrecognized.	Refer to the standard PCB configuration file and use only the documented error severity keywords.
6025	Missing attribute name.	The indicated line in the BeginChkRules section of the PCB configuration file is missing an attribute name.	Provide the appropriate attribute name.
6026	Missing routine name.	The indicated line in the BeginChkRules section of the PCB configuration file is missing a routine name.	Refer to the standard PCB configuration file and provide the appropriate procedure name.
6027	Missing object type.	The indicated line in the BeginChkRules section of the PCB configuration file is missing an object type.	Refer to the standard PCB configuration file and provide the appropriate object type keyword.
6028	Missing severity.	The indicated line in the BeginChkRules section of the PCB configuration file is missing an error severity.	Refer to the standard PCB configuration file and provide the appropriate error severity keyword.
6029	Missing fixcode.	The indicated line in the BeginChkRules section of the PCB configuration file is missing a fixcode.	Refer to the standard PCB configuration file and provide the appropriate fixcode.
6030	Missing number.	The indicated line in the BeginChkRules section of the PCB configuration file is missing an integer argument.	Refer to the standard PCB configuration file and provide the appropriate number.
6031	Bad fixcode: [fixcode].	The indicated line in the BeginChkRules section of the PCB configuration file has an illegal fixcode.	Refer to the standard PCB configuration file and use only the documented fixcodes.
6032	Bad number: [number].	The indicated line in the BeginChkRules section of the PCB configuration file has an illegal number.	Refer to the standard PCB configuration file and use an appropriate value.

6033	Incorrect number of arguments.	The indicated line in the BeginChkRules section of the PCB configuration file has an incorrect number of arguments.	Refer to the standard PCB configuration file and provide the appropriate arguments.
6034	Illegal character [character].	The attribute value indicated in the following message contains the illegal character shown.	Fix the attribute value, enable automatic fixups, add a GENVAL fixup, or change the PCB configuration file to allow this character. PCB Configuration File: CHKVAL_NAME_CHK, CHKVAL_LIST_CHK, GENVAL.
6035	Illegal length [length].	The attribute value indicated in the following message has illegal length.	Fix the attribute value, enable automatic fixups, add a GENVAL fixup, or change the PCB configuration file to allow this length. PCB Configuration File: CHKVAL_NAME_CHK, CHKVAL_LIST_CHK, GENVAL.
6036	Replacement character conflict.	The replacement character specified in a CHKVAL PCB configuration file line is itself an illegal character.	Specify a different replacement character. PCB Configuration File: CHKVAL_NAME_CHK, CHKVAL_LIST_CHK.
6037	Min length, max length conflict.	The minimum and maximum lengths specified in a CHKVAL PCB configuration file line are in conflict.	Specify lengths which do not conflict. PCB Configuration File: CHKVAL_NAME_CHK, CHKVAL_LIST_CHK, CHKVAL_REG_EXP.
6038	Expansion error n for string value.	The attribute value indicated in the following message cannot be expanded properly as a list.	Fix the attribute value or change the PCB configuration file. PCB Configuration File: CHKVAL_LIST_CHK.
6039	Illegal value [value].	The attribute value indicated in the following message is illegal.	Fix the attribute value, add a GENVAL fixup, or change the PCB configuration file to allow this value. PCB Configuration File: CHKVAL_ENUM_CHK, GENVAL.
6040	Missing character [character].	The attribute value indicated in the following message is missing the specified character.	Fix the attribute value, enable automatic fixups, add a GENVAL fixup, or change the PCB configuration file. PCB Configuration File: CHKVAL_FND_CHAR, GENVAL.
6041	Bad signal value [value].	The SIGNAL attribute value indicated in the message which follows has improper syntax.	Fix the attribute value. PCB Configuration File: CHKVAL_SIG_CHK.
6043	[second attribute] Always used following 6066, where 6066 specifies the first attribute in a conflict and 6043 specifies the second attribute in the conflict.	PADS Maker Schematic attributes for the same package or package pin are in conflict.	Fix the attribute(s), enable automatic fixups, or change the PCB configuration file. PCB Configuration File: CHKBRD_COMM_PIN3, CHKBRD_REF_CONFL, CHKBRD_VDVP_ATT.

6044	[no attribute] Always used following 6066, where 6066 specifies the first attribute in a conflict and 6044 specifies the second attribute in the conflict. In this case, the conflict arises because the second attribute is missing.	PADS Maker Schematic attributes for the same package or package pin are in conflict.	Fix the attribute(s), enable automatic fixups, or change the PCB configuration file. PCB Configuration File: CHKBRD_COMM_PIN3, CHKBRD_REF_CONFL, CHKBRD_VDVP_ATT.
6045	Repacking (attribute conflict).	Automatic fixup is repackaging the indicated PADS Maker Schematic component because of attribute conflicts with another component or common pin in the same package.	Ignore the message or change the PCB configuration file. PCB Configuration File: CHKBRD_REF_CONFL, CHKBRD_COMM_PIN3.
6046	Repacking (device conflict).	Automatic fixup is repackaging the indicated PADS Maker Schematic component because the DEVICE specified conflicts with that of another component in the same package.	Ignore the message. PCB Configuration File: CHKBRD_DEV_CONFL.
6047	Repacking (pin # not on symbol).	Automatic fixup is repackaging the indicated PADS Maker Schematic component because the pin numbers do not correspond to any valid slot.	Ignore the message. PCB Configuration File: CHKBRD_BAD_PIN_NUM.
6048	Repacking (pins from different slots).	Automatic fixup is repackaging the indicated PADS Maker Schematic component because pin numbers from more than one slot have been mixed together on the same gate.	Ignore the message. PCB Configuration File: CHKBRD_MIXED_SLOT.
6049	Repacking (duplicate slot).	Automatic fixup is repackaging the indicated PADS Maker Schematic component because another gate is already packaged in the same slot.	Ignore the message or change the PCB configuration. PCB Configuration File: CHKBRD_DUPL_SLOT.
6050	Repacking (net conflict).	Automatic fixup is repackaging the indicated PADS Maker Schematic component because the net connection on a common pin is different from the connection for the same pin on a different gate in the same package.	Ignore the message or change the PCB configuration file. PCB Configuration File: CHKBRD_COMM_PIN1, CHKBRD_COMM_PIN2, CHKBRD_COMM_PIN4.
6051	Duplicate pin number.	Duplicate pin numbers exist for different pins of the same gate, based on #, SIGNAL, and NC attributes.	Fix the pin number(s), or ignore the message if it is an intentional default SIGNAL override. PCB Configuration File: CHKBRD_DUPL_PIN.
6052	Duplicate name: [name].	A duplicate net name, gate name, or reference designator exists.	Remove the duplicate or change the PCB configuration file. PCB Configuration File: CHKBRD_DUPL_FNAME1, CHKBRD_DUPL_FNAME2, CHKBRD_DUPL_NNAME, CHKBRD_DUPL_RNAME.

6053	Duplicate attribute.	Duplicate attributes exist.	Try one of the following: <ul style="list-style-type: none"> Remove the duplicate or change the PCB configuration file. PCB Configuration File: CHKBRD_DUPL_ATTR, ALLOW_DUPL. Check Project from within PADS Maker Schematic, then try running ViewPCB again.
6054	Duplicate slot - n.	More than one PADS Maker Schematic component has been packaged in the same slot of the same package.	Repackage the gate(s), enable automatic fixups, or ignore the message if duplicate slots are allowed. PCB Configuration File: CHKBRD_DUPL_SLOT.
6055	Bad name: [name].	The indicated name is illegal for the reason specified in the preceding message.	Fix the name, enable automatic fixups, add a GENVAL fixup, or change the PCB configuration file to allow this name. PCB Configuration File: CHKVAL, GENVAL.
6056	Bad attribute name.	The indicated attribute name is illegal for the reason specified in the preceding message.	Fix the attribute name, enable automatic fixups, add a GENVAL fixup, or change the PCB configuration file to allow this attribute name. PCB Configuration File: CHKVAL, GENVAL.
6057	Bad attribute value.	The indicated attribute value is illegal for the reason specified in the preceding message.	Fix the attribute value, enable automatic fixups, add a GENVAL fixup, or change the PCB configuration file to allow this attribute value. PCB Configuration File: CHKVAL, GENVAL.
6058	PADS Maker Schematic attribute overriding.	Automatic fixup is updating a ViewPlace attribute to be consistent with PADS Maker Schematic.	Ignore the message or change the PCB configuration file. PCB Configuration File: CHKBRD_VDVP_ATT.
6059	[ViewPlace attribute] Always used following 6066, where 6066 specifies the PADS Maker Schematic attribute in a conflict and 6059 specifies the ViewPlace attribute in the conflict.	PADS Maker Schematic attributes for the same package or package pin are in conflict. <input type="checkbox"/>	Fix the attribute(s), enable automatic fixups, or change the PCB configuration file. PCB Configuration File: CHKBRD_COMM_PIN3, CHKBRD_REF_CONFL, CHKBRD_VDVP_ATT.
6060	Changing name old name to new name.	Automatic fixup is updating an illegal name to a new legal name.	Ignore the message or change the PCB configuration file. PCB Configuration File: CHKVAL, GENVAL.
6061	Changing attribute name to new name.	Automatic fixup is updating an illegal attribute name to a new legal name.	Ignore the message or change the PCB configuration file. PCB Configuration File: CHKVAL, GENVAL.
6062	Changing attribute value to new value.	Automatic fixup is updating an illegal attribute value to a new legal value.	Ignore the message or change the PCB configuration file. PCB Configuration File: CHKVAL, GENVAL.

6063	Changing shape to shape.	Automatic fixup is updating a ViewPlace package shape to be consistent with the PADS Maker Schematic PKG_TYPE for the same package.	Ignore the message or change the PCB configuration file. PCB Configuration File: CHKBRD_VDVP_SHP.
6064	Changing device to device.	Automatic fixup is updating a ViewPlace package device name to be consistent with the PADS Maker Schematic device name for the same package.	Ignore the message or change the PCB configuration file. PCB Configuration File: CHKBRD_VDVP_ATT.
6065	Changing ViewPlace net to net.	Automatic fixup is updating a ViewPlace net connection to be consistent with the same connection in PADS Maker Schematic.	Ignore the message or change the PCB configuration file. PCB Configuration File: CHKBRD_VDVP_NET.
6066	Attribute conflicts with [attribute].	PADS Maker Schematic attributes for the same package or package pin are in conflict.	Fix the attribute(s), enable automatic fixups, or change the PCB configuration file. PCB Configuration File: CHKBRD_COMM_PIN3, CHKBRD_REF_CONFL, CHKBRD_VDVP_ATT.
6067	Refdes [refdes], device [device] conflicts with device [device].	The indicated PADS Maker Schematic components have conflicting DEVICE attributes specified for the same reference designator.	Fix the DEVICE attribute(s), repackage the gate(s), or enable automatic fixups. PCB Configuration File: CHKBRD_DEV_CONFL.
6069	Net [net] conflicts with Net [net].	The indicated PADS Maker Schematic pin-net connection conflicts with the pin-net connection indicated in the following message for the same pin of the same package.	Fix the connection(s), repackage the gate(s), enable automatic fixups, or change the PCB configuration file. PCB Configuration File: CHKBRD_COMM_PIN1, CHKBRD_COMM_PIN2, CHKBRD_COMM_PIN4, CHKBRD_VDVP_NET.
6070	test pin \$114.GND27: Net DGND-RED.	When you have a part with a high pin count (typically, a component of a hetero device), you can get two or more pins with the same pin number.	Check the pin numbers on each component of the hetero device. Note that the pcbnetlister doesn't check for duplicate pin numbers on different components, even if the hetero attribute exists. It only checks pin numbers on a component.
6071	ViewPlace shape conflict.	The indicated ViewPlace package has a shape which conflicts with the PADS Maker Schematic PKG_TYPE for the same package.	Enable automatic fixups. PCB Configuration File: CHKBRD_VDVP_SHP.
6072	ViewPlace device conflict.	The indicated ViewPlace package has a device which conflicts with the PADS Maker Schematic device name for the same package.	Enable automatic fixups. PCB Configuration File: CHKB85RD_VDVP_ATT.
6073	ViewPlace net [net] not connected in PADS Maker Schematic.	The indicated ViewPlace pin-net connection does not exist on the schematic.	Enable automatic fixups. PCB Configuration File: CHKBRD_VDVP_NET.

6074	Attribute added.	Automatic fixup is adding a required attribute.	Ignore the message or change the PCB configuration file. PCB Configuration File: REQUIRE, CHKBRD _ILL_PINSWAP.
6075	ViewPlace package added.	Automatic fixup is adding a new ViewPlace package to correspond to a package or reference designator addition in PADS Maker Schematic.	Ignore the message or change the PCB configuration file. PCB Configuration File: CHKBRD _VDVP_PKG.
6076	Missing [name] attribute.	The indicated required attribute is missing.	Add the attribute, enable automatic fixups, or change the PCB configuration file. PCB Configuration File: REQUIRE.
6077	Missing shape pin.	The indicated package pin is defined on the PADS Maker Schematic symbol for this package, but another PADS Maker Schematic symbol with the same PKG_TYPE is missing the same pin.	Examine the symbols with the same PKG_TYPE, add the missing pin to the symbol(s), or ignore or disable the message. PCB Configuration File: CHKBRD _MISSING_PIN.
6078	Missing ViewPlace package [REFDES].	There is no package in ViewPlace which corresponds to the indicated assigned PADS Maker Schematic reference designator.	Enable automatic fixups. PCB Configuration File: CHKBRD _VDVP_PKG.
6079	Hetero device attribute conflict.	Hetero device attribute conflict.	Check the attributes on all sections of the hetero device and correct them as necessary.
6080	ViewPlace net deleted.	Automatic fixup is deleting the indicated net in ViewPlace because it no longer exists in PADS Maker Schematic.	Ignore the message or change the PCB configuration file. PCB Configuration File: CHKBRD _VDVP_NET.
6081	Extra ViewPlace package.	The indicated ViewPlace package does not correspond to any assigned PADS Maker Schematic reference designator.	Enable automatic fixups. PCB Configuration File: CHKBRD _VDVP_PKG.
6082	Extra shape pin [pin].	The indicated package pin is not defined on the PADS Maker Schematic symbol for this package, but another PADS Maker Schematic symbol with the same PKG_TYPE defines this pin.	Add the missing pin to the symbol, or ignore or disable the message. PCB Configuration File: CHKBRD _EXTRA_PIN.
6083	Single pin net: [net].	The net on the indicated pin is not connected to any other pins and so cannot be routed by a layout system without additional connection information.	Connect the net properly, enable automatic fixups, or ignore or disable the message. PCB Configuration File: CHKBRD _SGL_PIN_NET. Also, if this message is the result of using the NC attribute to identify no-connect pins, this message may be averted by specifying NET NC in the General portion of the AttPassList section of the PCB configuration file.

6084	Bus pin on symbol.	A bus pin on a PADS Maker Schematic symbol is not being expanded into separate pins before netlisting.	Put BusExpansion Yes in the PCB configuration file. This is the default and should not ordinarily be changed. PCB Configuration File: CHKBRD _BUS_PIN.
6085	n empty slot(s), device [device].	The indicated package of the specified device type has the number of unused slots shown.	Ignore the message, fill the empty slots, or use the PCB configuration file CreateSpareGates option to create the spare gates on a new top-level schematic sheet and to make default input connections. PCB Configuration File: CHKBRD _UNUSED_SLOT.
6086	Not packaged.	The indicated PADS Maker Schematic component could not be packaged because of errors reported in previous messages.	Resolve the reported errors.
6087	Pins have conflicting numbers of slots.	Some of the pins on the indicated PADS Maker Schematic symbol have missing or extra pin numbers defined for some slots.	Fix the pin number attributes. PCB Configuration File: CHKBRD _BAD_SLOTCNT.
6088	Component pin # not on symbol.	The pin number on the indicated PADS Maker Schematic component pin does not match any pin number defined on the PADS Maker Schematic symbol.	Fix the pin number or enable the automatic fixup. PCB Configuration File: CHKBRD _BAD_PIN_NUM.
6089	Pins from different slots on one component.	The pin numbers on the indicated PADS Maker Schematic component have been mixed together from more than one gate.	Fix the pin numbers or enable the automatic fixup. PCB Configuration File: CHKBRD _MIXED_SLOT.
6090	Illegal pinswap.	Pins on the indicated PADS Maker Schematic component have been swapped and there is no PINSWAP attribute that indicates that this swap was legal.	Ignore or disable the message, verify or fix the pin numbers, or enable the automatic fixup. PCB Configuration File: CHKBRD _ILL_PINSWAP.
6091	Symbol [symbol] missing for hetero device [device].	The indicated PADS Maker Schematic component is part of a heterogeneous device which includes another PADS Maker Schematic symbol which cannot be found.	Verify that the HETERO attribute on the component is correct, and if necessary instantiate a spare gate for any PADS Maker Schematic symbol listed which does not appear elsewhere in the schematic. PCB Configuration File: CHKBRD _BAD_HETERO.

6092	Bad hetero device [device], symbol [symbol].	The indicated PADS Maker Schematic component(s) have different symbols but the same DEVICE attribute, and the HETERO attribute has not been properly specified; or the device library is aliased.	Fix any incorrect DEVICE attributes or remove the library alias. If this is really a heterogeneous device, add or correct the HETERO attribute on the symbol(s) or component(s) and include the names of all the symbols which make up the device. PCB Configuration File: CHKBRD_BAD_HETERO.
		You added or removed a HETERO attribute after you packaged the design. The PCB interface cannot change the component level attribute once the design has been packaged.	Remove the HETERO attribute and run pcbfwd again to repackage the design.
6093	Pin shorted.	The indicated PADS Maker Schematic component pin is directly tied to another pin on the same gate.	Ignore or disable the message, or fix any schematic problem indicated. PCB Configuration File: CHKBRD_PIN_SHORTED.
6094	Pin not connected.	The indicated PADS Maker Schematic component pin is not connected to anything.	Ignore or disable the message, connect the pin properly, or use an NC attribute to indicate a pin which is intentionally left unconnected. PCB Configuration File: CHKBRD_UNCON_PIN.
6097	Line too long.	The indicated line in the PowerPCB ASC or ECO file is too long.	Verify that the file has the correct format and was properly generated from PowerPCB.
6098	Missing keyword [keyword].	The expected keyword shown is missing at the indicated line of the PowerPCB ASC or ECO file.	Verify that the file has the correct format and was properly generated from PowerPCB.
6099	Ambiguous keyword [keyword].	The keyword at the indicated line of the PowerPCB DS ASC or ECO file is ambiguous.	Verify that the file has the correct format and was properly generated from PowerPCB.
6100	Unexpected keyword [keyword].	The unexpected keyword shown is present at the indicated line of the PowerPCB ASC or ECO file.	Verify that the file has the correct format and was properly generated from PowerPCB.
6101	[refdes.pin] is already connected to net [net].	An ECO record at the indicated line of the PowerPCB ECO file attempts to connect a pin to a net, but the pin already has a different net attached. The ECO file may be old, incorrect, or incomplete.	Fix the ECO file or remove this ECO record, update the schematic with the remaining ECOs, and generate a new ECO file by comparing a new forward interface ASC output file with the existing PowerPCB ASC database file using the PowerPCB ECOGEN program or the POWERPCB Check ASCII function.

6102	[refdes.pin] was not connected to net [net].	An ECO record at the indicated line of the PowerPCB ECO file attempts to disconnect a pin from a net, but the pin already is disconnected. The ECO file may be old, incorrect, or incomplete.	Fix the ECO file, or update the schematic with the ECO file, then generate a new ECO file by comparing a new forward interface ASC output file with the existing PowerPCB ASC database file using the PowerPCB ECOGEN program or the PowerPCB Check ASCII function.
6103	Net [net] already exists.	An ECO record at the indicated line of the PowerPCB ECO file attempts to rename a net, but another net already has the same name. The ECO file may be old, incorrect, or incomplete.	Fix the ECO file or remove this ECO record, update the schematic with the remaining ECOs, and generate a new ECO file by comparing a new forward interface ASC output file with the existing PowerPCB ASC database file using the PowerPCB ECOGEN program or the PowerPCB Check ASCII function.
6104	Package refdes already exists.	An ECO record at the indicated line of the PowerPCB ECO file attempts to rename a package, but another package already has the same reference designator. The ECO file may be old, incorrect, or incomplete.	Fix the ECO file or remove this ECO record, update the schematic with the remaining ECOs, and generate a new ECO file by comparing a new forward interface ASC output file with the existing PowerPCB ASC database file using the PowerPCB ECOGEN program or the PowerPCB Check ASCII function.
6105	Incomplete data.	The indicated line in the PowerPCB ASC or ECO file does not have the expected number of arguments.	Verify that the file has the correct format and was properly generated from PowerPCB.
6106	Net [net] not found.	The net referred to on the indicated line of the ECO file does not exist in the schematic. The file may be old, incorrect, or incomplete.	Verify that the file has the correct format and was properly generated from layout. Fix the file or remove the ECO record, update the schematic with the remaining ECOs, and generate a new ECO file by comparing a new forward interface output file with the existing database file (such as with the PowerPCB ECOGEN command or the PowerPCB Check ASCII function).

6107	Package [refdes] not found.	The package referred to on the indicated line of the database or ECO file does not exist in the schematic. The file may be old, incorrect, or incomplete.	Verify that the file has the correct format and was properly generated from layout. Fix the file or remove the ECO record, update the schematic with the remaining ECOs, and generate a new ECO file by comparing a new forward interface output file with the existing database file (such as with the PowerPCB ECOGEN command or the PowerPCB Check ASCII function).
6108	Pin [refdes, pin] not found.	The package pin referred to on the indicated line of the PowerPCB ECO file does not exist in the schematic. The file may be old, incorrect, or incomplete.	Fix the file or remove the ECO record, update the schematic with the remaining ECOs, and generate a new ECO file by comparing a new forward interface ASC output file with the existing PowerPCB ASC database file using the PowerPCB ECOGEN program or the PowerPCB Check ASCII function.
6109	Slot [refdes.slot] not found.	The package and/or slot referred to on the indicated line of the PowerPCB ECO file does not exist in the PowerPCB ASC file or does not correspond to a slot defined on the corresponding symbol.	Verify that the files have the correct formats and were properly generated from POWERPCB. Fix the files or remove the ECO record, update the schematic with the remaining ECOs, and generate a new ECO file by comparing a new forward interface ASC output file with the existing PowerPCB ASC database file using the PowerPCB ECOGEN program or the PowerPCB Check ASCII function.
6110	Could not write file [filename].	The indicated file could not be written.	Verify that the directory exists and that the correct access modes are set.
6112	Could not create new [device] package [refdes].	An ECO record at the indicated line of the PowerPCB ECO file attempts to create a new package, but the appropriate symbol cannot be determined or loaded.	Verify that the appropriate symbol is in an accessible PADS Maker Schematic library directory and/or provide a mapping for this device or reference designator to the appropriate symbol in the BeginNewSymRules section of the PCB configuration file.

6113	Device [device] has [n] shapes.	An attempt was made to generate device descriptions for PowerPCB in the form of a PowerPCB ASCII PARTTYPE library file, but the indicated device has too many associated PKG_TYPE attributes in DxPADS Maker for the PowerPCB software to handle it, or there is no associated PKG_TYPE for this device in PADS Maker Schematic.	Reduce the number of PKG_TYPEs for this device, do not generate PARTTYPEs, or use the REQUIRE PCB configuration file keyword to locate any missing PKG_TYPE attributes.
6116	Ignoring single pin net:[net].	Automatic fixup is treating the indicated pin as unconnected, because its net did not connect to any other pins. The net will not appear in the netlist file.	Connect the net properly or ignore or disable the message. PCB Configuration File: CHKBRD_SGL_PIN_NET.
6117	Bad pinswap definition.	The indicated PINSWAP attribute has improper syntax.	Fix the attribute syntax. PCB Configuration File: CHKBRD_BAD_PINSWAP.
6118	Unmatched parenthesis.	The indicated PINSWAP attribute has an unmatched parenthesis.	Fix the attribute syntax. PCB Configuration File: CHKBRD_BAD_PINSWAP.
6119	Mismatched parentheses.	The indicated PINSWAP attribute has mismatched parentheses.	Fix the attribute syntax. PCB Configuration File: CHKBRD_BAD_PINSWAP.
6120	Bad pin name:[pin].	The indicated pin name in the PINSWAP attribute is invalid because no symbol pin exists with that name.	Fix the PINSWAP attribute. PCB Configuration File: CHKBRD_BAD_PINSWAP.
6130	Duplicate pin number [#] in hetero 3 device.	A pin number is duplicated between two or more symbols of a hetero type 3 device.	Remove duplicate pins from symbol(s).
6138	Multiple Symbols <symname>device<devicename> are inconsistent.	This error occurs whenever there are multiple instantiations of inconsistent symbols with the same device name.	Make sure that inconsistent symbols with the same device name have matching pins.
6265	File <design>.baf, line 6037: Skipping record for F176 during change processing bad lwr processing.	N/A.	N/A.
6284	Design attribute PKG_TYPE being changed from <type1> to <type2> double check.	N/A.	N/A.
6521	Parse error.	A parsing error occurred while reading either the comps, inst or net files.	Check these files for possible errors.
6523	Could not rename refdes of gate %s to %s, lwrStat = %d.	A component rename could not be done.	Check the back annotation file to ensure that it has the correct rename information.

6525	Could not update internal data for a pin DGND, net GND1.	This is a HETERO Type 2 problem. In the function dictionary, sometimes there are multiple instances of the same function representing the different components of the hetero device and the order in which the instances appear may be different, which could affect the back annotation. The PCB netlister may not be able to correctly define which instance it should be looking at.	Using the error message, identify the function in question and determine if multiple instances exist for the function. If so, try rearranging the instance numbers of the instances in the function dictionary so the netlister looks at the correct one when back annotating information from the PCB layout. As an example, a function dictionary contains these two entries: \$81133 F81 \$81120 F81 Rearrange the entries to look like the following: \$81120 F81 \$81133 F81
6530	Could not update component <#>.	Case sensitivity in pin numbers—the gates file from pcbfwd has the alphanumeric pin numbers in upper case, but the gates file from Board Station has the alphanumeric pin numbers in lower case. The Board Station library geometry file needs to have the pin numbers/names in upper case because the Board Station output gates file is derived from that Board Station library geometry file.	Change the Board Station geometry or gates file to include upper case alphanumeric pin numbers.
6531	Could not find refdes [refdes#] in schematic database.	A specified package could not be found in the schematic.	Check schematic to ensure specified package exists.
6532	Could not find component %s in schematic database.	A specified component could not be found in the schematic.	Check schematic to ensure specified component exists.
6577	Signal pin on device not specified in POWERS or GROUNDS.	Assuming the signal is specified as a SIGNAL on a component. It is not specified as a Power or Ground in the pcb configuration file.	Specify the signal as a power or ground in the pcb configuration file.
6631	Writing partslist file [filename].	The partslist output file is being written.	None, for informational purposes only.
6632	Attribute [attribute_name] has conflicting values on same PKG [pkg_name].	More than one value is assigned to a single attribute within one package.	This may or may not require attention. If the MULTIPLE or ITEMIZER keyword lists several possible values for an attribute, such as package manufacturer, ignore this message. If more than one value for the attribute is to be disregarded, remove all values but the one you want from the package.
6633	More than one MULTIPLE_KEY [value] chosen for same pkg [pkg_name].	There is more than one value assigned to MULTIPLE_KEY for a single package.	Remove or comment out all but one MULTIPLE_KEY value.

6634	Pagination ignored, exceeds allowable line width.	The date, design name, or page number stamp exceeds allowable line width.	Increase the width for the last field.
6635	No REV# Attributes found on BOARD.	The REVISION_ON keyword is set to yes, and there are no REV# attributes found on the board.	Two methods exist to fix this problem: <ul style="list-style-type: none"> • Attach one or more REV# attributes to the board. • Change the REVISION_ON keyword setting to no.
6636	No BAN# Attributes found on BOARD.	The BANNER_ON keyword is set to yes, and there are no BAN# attributes found on the board.	Two methods exist to fix this problem: <ul style="list-style-type: none"> • Attach one or more BAN# attributes to the board. • Change the BANNER_ON keyword setting to no.
6637	Internal Error: List - Table out of sync.	An internal malfunction has occurred.	This internal error should not occur with the end user. However, if it does, record your actions prior to the error and contact Mentor Graphics Technical Support.
6638	Header line count exceeded 3-lines. Ignored field label : [field_label].	The maximum line count for a standard header is three, and this has been exceeded.	For more than three lines in a header, use ALT_HEADER.
6639	Internal Warning: UNKNOWN special data field.	An internal malfunction has occurred.	This internal error should not occur with the end user. If it does, however, record your actions prior to the error and contact Mentor Graphics Technical Support.
6640	Value [string] truncated to fit the data field defined.	The number of characters in the value string exceeds the allowable field width.	Increase the field width for the specified value.
6641	SUM [number] truncated to fit the data field defined.	The number of digits in the summed value exceeds the allowable field width.	Increase the field width for SUM.
6642	Unbalanced double quotes in .ini file for field: [field_label].	A specified field label is missing a double quote character.	Add a double quote character where one is missing.
6643	Bad FIELD_DEF type [value] Changed to SINGULAR.	The .ini file specifies a field definition mode other than SINGULAR, ITEMIZER, or MULTIPLE, or the syntax is incorrect.	Check for correct syntax in the field definitions, and make sure the keywords are nothing other than SINGULAR, ITEMIZER, or MULTIPLE.
6644	Label exceeds allowable line width. Ignored field label : [field_label].	The last column label extends beyond the allowable line width in a standard header.	Use ALT_HEADER, or modify label string or field width.
6645	Adjusted ITEM/QTY value [value] to allowable width.	ITEM/QTY value exceeds the allowable field width.	Increase the field width.
6646	No Standard or Field definition found in the .ini file.	The partlist.ini file is missing the Standard definition or Field definition sections.	The Standard and Field definitions are required to properly generate a parts list. Add these definitions sections appropriately.

6647	Unable to open file_name for writing.	The specified file cannot be found or does not have appropriate permissions.	Verify that the specified file exists in the appropriate location under the correct name and with appropriate permissions.
6802	File %s not found.	The pkgid.xrf file is missing from the project area.	Ensure this file is present in the project area and rerun ViewPCB.
6803	Line %d in file %s has wrong number of arguments.	The specified line read from the pkgid.xrf file has an incorrect format.	Review the specified line for formatting errors.
6805	Illegal value for attribute PCAD_SECTNUM on symbol %s.	An illegal value has been specified for the PCAD_SECTNUM attribute.	Change the value of this attribute to a legal value.
6822	Could not swap net %s with %s. Manually fix!	Two nets slated for renaming could not be located on the schematic.	Manually locate and change these net names.
7100	Unable to graphically represent the gate swap between package [package1] and package [package2].	You have drawn the entire package on the schematic, rather than drawing each of the individual gates. The pcbfwd command cannot gate swap unless individual gates are drawn.	Instantiate the smallest possible representation of the gates that make up the package.
7108	Pinswap attribute requires at least two pins.	You have assigned a pinswap attribute in the following form: PINSWAP=[Pin#m].	Correct the value of the attribute to include at least two pins. For example: PINSWAP=[Pin#m,Pin#n].
7127	The Schematic units are inconsistent with the ASCII file units.	This message appears during back-annotation. The units were changed in layout. Therefore, the units contained in the back-annotation file do not match the units in the schematic.	The layout house must export an ASCII file with matching units.
7128	Unsupported Version.	You are attempting to run a pcbfwd or pcbbck command using a PCB configuration file for either POWERPCB Perform, PADS2000 or PowerPCB 1.	You can continue to use the PCB configuration file from any of these versions, if you update the netlist file header line. This is the first line in the PCB configuration file. It should read either: <ul style="list-style-type: none"> • NetFileHeader !PADS-POWERV2.0 • NetFileHeader !PADS-POWERV3.0
7129	Warning: Attribute on [Object] is not in AttPassList.	You have placed an attribute that is not in the standard AttPassList into the PCB configuration file.	Add the attribute to the AttPassList in the PCB configuration file, set ExcludeName 7129 in the PowerPCB.CFG file, or ignore the warning.
7131	Space not allowed in value field for attribute [attribute].	An attempt was made to back-annotate an attribute value that contains a space.	Replace the space with an underscore or other appropriate character or remove the attribute from the POWERPCB ECO file.

7139	Error: device attribute has conflicting values.	The attribute on a device has different value in PADS Maker Schematic than in the layout system—the symbol attribute value does not match the component value.	Change the attribute value to be the same in PADS Maker Schematic and in the layout system.
7202	Warning: rpack_tst net VDD: NC attribute attached to multi-pin net.	N/A.	N/A.
7206	Hetero symbol missing from hetero package on device <devicename>.	Placing a device attribute on only one part, and not on another part or all parts of the device. For example, if HETERO=(TYPE3),(TYPE3A) is used, and only symbol TYPE3 is placed on a part, the following warning message is displayed when a check is performed: PCB: Error 7206: New pkg U1: Hetero symbol missing from hetero package on device 'TYPE3'.	Make sure to specify the same attributes for all symbols in the hetero package.
7300	Component [component] not found in schematic <schematic>.	A component is referenced in back-annotation that does not exist on the schematic. Likely cause is modification to the schematic during layout.	Resolve the inconsistency between PADS Maker Schematic and layout.
7307	Net Props Write Error.	This occurs when performing a back annotation from Expedition when a MATCH_GROUP attribute is attached to a group of nets that does not have the MATCH_TOLERANCE attribute applied to them. This is an unsupported attribute.	Do not use the MATCH_GROUP attributes.
7463	CES database update failed.	N/A.	N/A.
7456	Error loading %s interface.	System level error. A dll for the interface cannot be loaded.	Conflicting versions of ePD installations and WDIR directory location can cause DLL file to be misplaced.
7600	Symbol'%s' Attribute '%s=%s' was ignored.	A symbol attribute has been overridden by a PDB attribute.	Remove the attribute on the symbol.
7601	Could not add attribute %s on object %s.	Problem occurred trying to add an attribute; this attribute will not be added to the object.	Internal Error.
7602	Pin '%s' is not defined in PDB Part '%s'."	A PADS Maker Schematic symbol pin label was not found in a PDB part definition.	Match all the pin labels from a PADS Maker Schematic symbol with the pin labels defined for a PDB part.
7603	Inconsistencies found between Dx and PDB No Connect definitions for Parts Number %s.	No Connect pins differ on PADS Maker Schematic and PDB parts.	Resolve differences between PADS Maker Schematic and PDB parts.
7604	Initializing PDB Partition.	Invalid central library or no partitions were found on a PDB.	Information only. No action required.

7605	Initializing PDB Partition Part Numbers.	No parts defined in PDB Partition.	Information only. No action required.
7606	Symbol '%s' did not match the PDB part definition of Part Number '%s'."	The PADS Maker Schematic symbol did not match the matching PDB part symbol.	Resolve differences between PADS Maker Schematic and PDB parts.
7607	No Pin Mapping definitions found for Symbol '%s' in Part Number '%s'."	Error obtaining pin maps from the PDB. The PDB may be defective.	Fix the problem in the PDB.
7608	Pin swap definition for device '%s' does not match PDB.	The pinswap defined for a PADS Maker Schematic component did not match the pinswap definition for a matching PDB part.	Resolve differences between PADS Maker Schematic and PDB parts.
7609	Using central library '%s' for packaging.	Specifies the central library being used when packaging.	Information only. No action required.
7610	Inconsistent Pin Definitions between PADS Maker Schematic and PDB Symbol '%s'.	A PADS Maker Schematic symbol pin label was not found or did not match a pin label in a PDB part definition.	Resolve differences between PADS Maker Schematic and PDB.
7611	Pin Type did not match on pin '%s' for PADS Maker Schematic and PDB Symbol '%s'.	Inconsistent pin type definitions between the PADS Maker Schematic symbol and the equivalent PDB symbol.	Resolve differences between PADS Maker Schematic and PDB.
7613	PADS Maker Schematic Pin Number '%s' did not match the PDB definition of Part '%s'.	PADS Maker Schematic package pin number did not match the matching PDB part definition.	Resolve differences between PADS Maker Schematic and PDB.
7614	PADS Maker Schematic Pin Name '%s' did not match the PDB definition of Part '%s'.	PADS Maker Schematic package pin name did not match the matching PDB part definition.	Resolve differences between PADS Maker Schematic and PDB.
7615	NC Pin '%s' PDB mapping is not defined for Part Number '%s'.	PADS Maker Schematic NC package pin is not defined in the matching PDB part.	Resolve differences between PADS Maker Schematic and PDB parts.
7616	Signal Pin '%s' PDB mapping is not defined for Part Number '%s'.	PADS Maker Schematic Signal package pin is not defined in the matching PDB part	Resolve differences between PADS Maker Schematic and PDB parts.
7617	PADS Maker Schematic Package Shape '%s' was not found in the PDB definition of Part Number '%s'.	Invalid PADS Maker Schematic Package Shape. Package type was not defined in the matching PDB part.	Change the PADS Maker Schematic PKG_TYPE attribute value to match one of the cells defined in the PDB part, or Add a cell that matches the definition of the PADS Maker Schematic PKG_TYPE attribute value.
7618	Inconsistencies found between PADS Maker Schematic and PDB parts.	Mismatching of Symbols, pins, pinswaps, slot or package types.	Resolve differences between PADS Maker Schematic and PDB parts.
7619	Inconsistencies found between Dx and PDB parts.	Parts definitions in PADS Maker Schematic and PDB do not match.	Resolve differences between PADS Maker Schematic and PDB parts.
7620	Invalid Generic Symbol %s.	Generic symbol defined in PADS Maker Schematic is not defined in PDB.	Add generic symbol definition to PDB.
7621	Part %s was not found in PDB.	PADS Maker Schematic part not defined in PDB.	Add PADS Maker Schematic part to PDB.

7622	PDB Part %s contains invalid symbol swap groups defined.	A part in the PDB has multiple symbol swap groups (multiple subgates). This is not supported in PADS Maker Schematic.	Use only PADS Maker Schematic compatible parts.
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